

Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 42390P2445C3	Application Number: Not Yet Assigned
Title: A METHOD AND APPARATUS FOR PERFORMING MULTIPLY-ADD OPERATIONS ON PACKED DATA		First Named Inventor: Alexander D. Peleg	
		Filing Date: Herewith	Page 1 of 4

JC828 U.S. PTO
 09/989736
 11/19/01

U.S. PATENT DOCUMENTS						
Exam. Initial*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (If known)			
<i>hu</i>		6,035,316		Peleg, et al.	3/7/2000	
		5,983,256		Peleg, et al.	11/9/1999	
		5,859,997		Peleg, et al.	1/12/1999	
		5,793,661		Dulong, et al.	8/11/1998	
		5,721,892		Peleg, et al.	2/24/1998	
		5,576,983		Shiokawa	11/19/1996	
		5,528,529		Seal	6/18/1996	
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		Office ³	Number ⁴	Kind Code ⁵ (If known)				

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<i>du</i>		5,420,815		Nix, et al.	5/30/1995	
		5,325,320		Chiu	6/28/1994	
		5,321,644		Schibinger	6/14/1994	
		5,293,558		Narita, et al.	3/8/1994	
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<i>du</i>		4,707,800		Montrone, et al.	11/17/1987	
		4,498,177		Larson	2/5/1985	
		4,418,383		Doyle, et al.	11/29/1983	
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OTHER ART - NO PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²
A ↓ V		International Search Report for PCT/US96/12799, Dated 11-26-1996 (1 page)	
		J. SHIPNES, <u>Graphics Processing with the 88110 RISC Microprocessor</u> , IEEE (1992), pp 169-174.	
		<u>MC88110 Second Generation RISC Microprocessor User's Manual</u> , Motorola Inc. (1991)	
		<u>Errata to MC88110 Second Generation RISC Microprocessor User's Manual</u> , Motorola Inc. (1992) pp 1-11	
		<u>MC88110 Programmer's Reference Guide</u> , Motorola Inc. (1992), pp 1-4.	
		<u>i860™ Microprocessor Family Programmer's Reference Manual</u> , Intel Corporation (1992), Ch. 1, 3, 8, 12.	
		R.B. LEE, <u>Accelerating Multimedia With Enhanced Microprocessors</u> , IEEE Micro (April 1995), pp 22-32.	
		<u>TMS320C2X User's Guide</u> , Texas Instruments (1993) pp3-2 through 3-11; 3-28 through 3-34; 4-1 through 4-22; 4-41; 4-103; 4-119 through 4-120; 4-150 through 4-151.	
		L. GWENNAP, <u>New PA-RISC Processor Decodes MPEG Video</u> , Microprocessor Report (Jan. 1994), pp 16, 17.	
		SPARC Technology Business, <u>Ultra SPARC Multimedia Capabilities On-Chip Support for Real-Time Video and Advanced Graphics</u> , Sun Microsystems (Sept. 1994)	
		Y. KAWAKAMI et al., <u>LSI Applications: A Single-Chip Digital Signal Processor for Voiceband Applications</u> , Solid State Circuits Conference, Digest of Technical Papers; IEEE International (1980)	
		B. CASE, <u>Philips Hopes to Displace DSPs with VLIW</u> , Microprocessor Report (Dec. 1994), pp 12-18.	
	N. MARGULIS, <u>i860™ Microprocessor Architecture</u> , McGraw Hill, Inc. (1990), Ch. 6, 7, 8, 10, 11.		
	<u>Pentium Processor User's Manual, Volume 3: Architecture and Programming Manual</u> , Intel Corporation (1993), Ch. 1, 3, 4, 6, 8, and 18.		
Examiner Signature	Date Considered		9/20/95

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		Applicant: Alexander D. Peleg				
		Filing Date: November 19, 2001				
Form PTO-1449 (Modified)						
US Patent Documents						
Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date
<i>man</i>	02/15/72	3,641,736	Dwyer			
	08/19/75	3,900,724	McIver			
	03/02/76	3,941,990	Rabasse et al.			
	09/17/85	4,542,476	Nagafuji, et al.			
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	07/26/88	4,760,545	Inagami et al.			
	03/07/89	4,811,269	Hirose et al.			
	12/26/89	4,890,218	Bram			
	07/31/90	4,945,479	Rusterholz et al.			
	11/20/90	4,972,362	Elkind et al.			
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	11/30/92	5,161,247	Murakami et al.			
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	05/11/93	5,210,711	Rossmere, et al.			
	08/31/93	5,241,493	Chu et al.			
	04/12/94	5,303,355	Gergen et al.			
05/10/94	5,311,508	Buda, et al.				
07/05/94	5,327,369	Ashkenazi				
Foreign Patent Documents						
Examiner's Initials	Date	Document Number	Country	Class	Sub- Class	Translation
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)						
Examiner <i>man</i>			Date Considered <i>9/2005</i>			

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Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date
[Signature]	02/14/95	5,390,135	Lee et al.			
	06/20/95	5,426,598	Hagihara			
	05/07/96	5,515,520	Hatta et al.			
	11/26/96	5,579,253	Lee et al.			
	12/31/96	5,590,365	Ide et al.			
	02/25/97	5,606,677	Balmer et al.			
	06/24/97	5,642,306	Mennemeir			
	09/30/97	5,673,427	Brown et al.			
	12/23/97	5,701,508	Glew et al.			
	04/21/98	5,742,840	Hansen et al.			
	06/09/98	5,765,037	Morrison et al.			
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	08/11/98	5,793,661	Dulong, et al.			
	08/11/98	5,794,060	Hansen et al.			
	08/11/98	5,794,061	Hansen et al.			
	09/01/98	5,802,336	Peleg et al.			
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<i>men</i>		09/15/98	5,809,321	Hansen et al.			
		10/06/98	5,819,101	Peleg et al.			
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		01/25/00	6,018,351	Mennemeir, et al.			
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		05/21/00	6,041,403	Parker, et al.			
		09/19/00	6,122,725	Roussel et al.			
✓		11/28/00	6,154,831	Thayer et al.			
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US Patent Documents						
New		01/23/01	6,178,482	Sollars		
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		05/08/01	6,230,253	Roussel et al.		
		05/08/01	6,230,257	Roussel et al.		
		05/15/01	6,233,671	Abdallah et al.		
		07/10/01	6,260,137	Fleck, et al.		
		09/25/01	6,295,599	Hansen et al.		
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		06/24/03	6,584,482	Hansen et al.		
		11/04/03	6,643,765	Hansen et al.		
	✓		04/20/04	6,725,356	Hansen et al.	
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New	4/23/1997	DE 3636106	Germany			
<div style="border-left: 1px solid black; height: 100%; width: 10px; margin: 0 auto;"></div>	10/14/1999	DE 19914617	Germany			
	10/7/1999	WO 99/50740	PCT			
	7/3/1997	WO 97/23821	PCT			
	12/01/00	GB 2339040	Great Britian			
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D		06/29/97	WO 97/22923	PCT			
J		06/29/97	WO 97/22924	PCT			
J		06/27/97	WO 97/22921	PCT			
J		03/06/97	WO 97/08608	PCT			
		03/19/90	JP 3-268024	Japan			
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✓		Samsung Electronics, "21164 Alpha Microprocessor Data Sheet", 1997.				
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		"Silicon Graphics Introduces Compact MIPS@ RISC Microprocessor Code for High Performance at a Low Cost", Oct. 21, 1996, downloaded from the Website webmaster@www.sgi.com, pp. 1-2				
✓		Killian, Earl, "MIPS Extension for Digital Media", Silicon Graphics, pp. 1-10				
✓		"MIPS V Instruction Set", pp. B1 to B7.				
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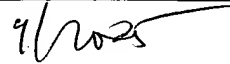
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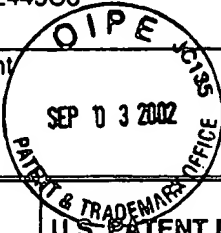
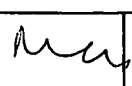
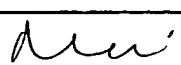
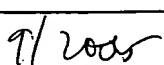
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US Department of Commerce Patent and Trademark Office Form PTO-1449 (Modified)		Atty. Docket No.: 42390P2445C3		Application No.: 09/989,736		
		Applicant: Alexander D. Peleg				
		Filing Date: November 19, 2001				
US Patent Documents						
Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date
Foreign Patent Documents						
Examiner's Initials	Date	Document Number	Country	Class	Sub- Class	Translation
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)						
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Page 1 of 1				Applicant: Alexander D. Peleg, et al.			
List of Patents and Publications Statement (Use several sheets if necessary)				Filing Date: August 31, 1995			
REFERENCE DESIGNATION		U.S. PATENT DOCUMENTS					
Examiner Initials		Document No.		Class	Sub- Class	Filing date if appropriate	
	AA						
	AB						
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